

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Serial No.: 10/560,706 Confirmation No.: 9357
Applicant(s): Takefumi NISHIMUTA et al. Group Art Unit: 2809
Examiner: Belousov, Alexander
Filed: July 24, 2006 Customer No.: 27123
For: MIS TRANSISTOR AND CMOS TRANSISTOR

RESPONSE TO REQUIREMENT FOR RESTRICTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated September 5, 2007 constituting a requirement for restriction in the above-identified application, election is made to prosecute the claims in the Examiner's Embodiment I shown in Fig. 6. Applicant believes that Claims 1-13 are readable on the elected species. The election is made without traverse.

Action on the merits of claims 1-13 is respectfully requested.

Respectfully submitted,
MORGAN & FINNEGAN, L.L.P.

Dated: October 3, 2007



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